

In the Drawing:

Please amend the drawing as illustrated in red in the accompanying print to amend Fig. 1 and Fig. 5A so that element 116 calls for “DSP Processor with different memories including fast on-chip memory.”



Figure 1: Development Flow

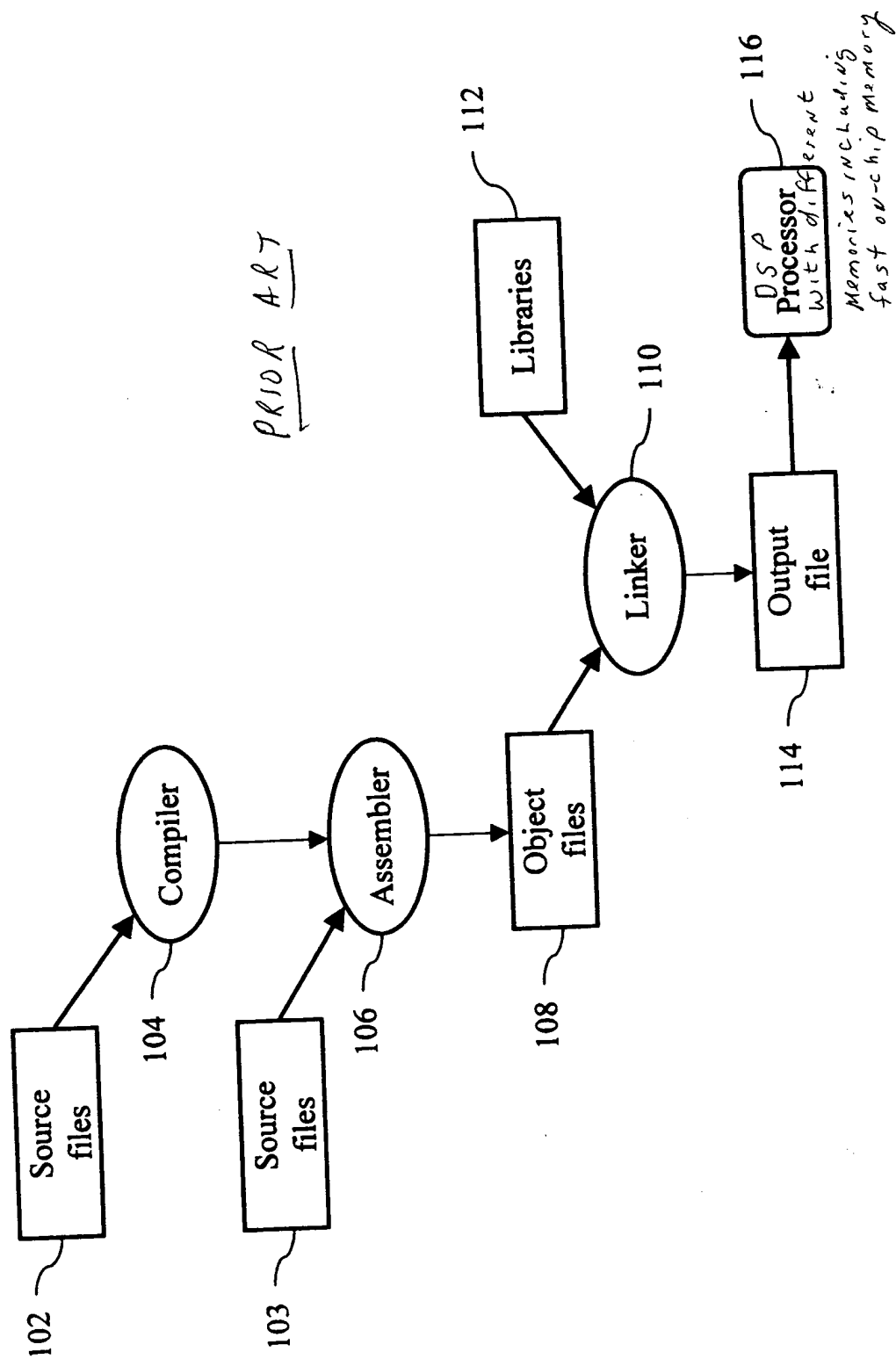




Figure 5A: Visual Linker Apparatus

